Description:
The IBM 129 Card Data Recorder is an operator-oriented, stand-alone, key entry machine used to prepare 80-column punched cards. Three basic models are available:
- Combination punch and verify
- Punch-only with printing
- Combination punch and verify with printing
The IBM 129 utilizes a buffered storage providing increased throughput and capabilities over present keypunch and verifier machines.

Features:
- Key in data while machine is performing such automatic functions as card feed, register, skip and duplication.
- Key in data while previous card is being punched.

Reliability:
- Spring-loaded print interposer unit minimizes print mechanism damage when duplicating program cards or invalid information.

Technology:
- P Cams, Program Cam Contacts, and CF Cams replaced by optical system. Pin sense unit replaced by optical reading system.
- Buffered storage of programs eliminates program drum and starwheels.

Serviceability:
- Direct punch switch isolates electronic circuitry to allow diagnosis of mechanical faults.
- Analysis Procedure Manual (APM), a diagnostic guide, assists the CE in locating the failing field replaceable component.
- The ability to punch out information in storage allows the CE to determine program and data contents of storage at any given point.

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IBM 129 CARD DATA RECORDER

- Erase and correct a keyed error before punching.
- Insertion of automatic left zero or left blank without field size limitation.
- Store and control of six program levels from buffered storage. One additional program level of 80-column alpha.
- Immediate make-over of error cards during verification. Hand-insert blank card behind error card and punch out corrected card.
- P Cams, Program Cam Contacts, and CF Cams replaced by optical system. Pin sense unit replaced by optical reading system.
- Buffered storage of programs eliminates program drum and starwheels.

Technology:
- Card reading and machine function timings are provided by optical systems. These systems use light sources and light sensitive transistors.
- Machine functions and memory are controlled by 100 ns Solid Logic Technology Dense (SLD). This technology has a proven reliability and ease of servicing.
- Buffered Memory is comprised of Field Effect Transistor (FET) Technology.

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